

Appl. No. 10/619,852  
Amtd. Dated 25 October 2004  
Reply to Notice of Non-Compliant Amendment of October 21, 2004

### AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application:

#### Listing of Claims:

1. (Currently Amended) An electrical circuit for producing a stable voltage at a circuit output, comprising:  
an operational amplifier;  
said amplifier having an inverting input node and an amplifier output node;  
at least one light-emitting diode in series electrically connected to said amplifier output node;  
at least two forward-biased photovoltaic diodes arranged in electronically isolated diode pairs;  
each of said isolated diode pairs positioned in uniform operational proximity to one of said at least one light-emitting diode;  
each isolated diode pairs comprised of a first diode set and a second diode set;  
each of said first diode set electrically connected in series in a chargeable closed circuit to said inverting input node; and  
each of said second diode set electrically connected in series to said circuit output.
2. (Original) The electrical circuit in claim 1 further comprising:  
a reference voltage source;  
said first diode set connected to said reference voltage source.

Appl. No. 10/619,852  
Amtd. Dated 25 October 2004  
Reply to Notice of Non-Compliant Amendment of October 21, 2004

- Part 1/26
3. (Original) The electrical circuit in claim 2 where:  
said reference voltage source providing an adjustable voltage.
  4. (Original) The electrical circuit in claim 1 further comprising:  
a capacitor electrically connected to said amplifier output node and said inverting input node.
  5. (Original) The electrical circuit in claim 4 further comprising:  
said first diode set having a number of individual diodes in series; and  
said inverting input node connected to first diode set intermediate an equal number of said individual diodes.
  6. (Original) The electrical circuit in claim 1 further comprising:  
a balanced pair of resistors connected in parallel with said first diode set; and  
said inverting input node connected to said first diode set intermediate said balanced pair of resistors.
  7. (Original) The electrical circuit in claim 1 further comprising:  
equivalent resistance connected in parallel with each of said first diode set and said second diode set.

Appl. No. 10/619,852  
Amtd. Dated 25 October 2004  
Reply to Notice of Non-Compliant Amendment of October 21, 2004

8. (Original) The electrical circuit in claim 1 further comprising:

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resistance connected intermediate said amplifier output node and said at least one light-emitting diode.

9. (Currently Amended) A method for producing a stable voltage comprising:

initiating a drive current through a drive circuit, said drive circuit containing an operational amplifier, said amplifier having an inverting input node and an amplifier output node, at least one light-emitting diode in series and said at least one light-emitting diode electrically receiving said drive current from said amplifier output node;

inducing at least two electrically isolated driven currents in at least one electrically isolated controlled circuit and at least one electrically isolated controlling circuit, said controlled circuit having at least one forward-biased photovoltaic diode, each said at least one photovoltaic diode arranged in electrically isolated pairs with at least one photovoltaic diode of said controlling circuit each, each said at least one photovoltaic diode of each of said isolated diode pairs positioned in uniform operational proximity to one of said at least one light-emitting diode, each isolated diode pairs comprised of a controlling diode and a controlled diode, each of said controlling diodes electrically connected in series in a chargeable closed circuit to said inverting input node and each of said controlled diodes electrically connected in series to said circuit output;

stabilizing said electrically isolated driven current of each said at least one electrically isolated controlled circuit by adjusting said driven current with said drive current corrected by said operational amplifier for a correcting signal at said inverting input node.

Appl. No. 10/619,852  
Amtd. Dated 25 October 2004  
Reply to Notice of Non-Compliant Amendment of October 21, 2004

10. (Original) The method of claim 9 wherein:

said controlling diodes generate said correcting signal to said inverting input node.

11. (Original) The method of claim 9 wherein:

said correcting signal is 0 when circuit is in a stable condition.

12. (Original) An electrical circuit for producing a stable voltage at a circuit output,

comprising:

an operational amplifier;

said amplifier having an inverting input node and an amplifier output node;

at least one light-emitting diode in series electrically connected to said amplifier output node;

at least two photovoltaic diodes arranged in electronically isolated diode pairs;

each of said isolated diode pairs positioned in uniform operational proximity to one of said at least one light-emitting diode;

each isolated diode pair comprised of a first diode set and a second diode set;

a balanced pair of resistors connected in parallel with said first diode set;

a potentiometer connected to said first diode set intermediate said balanced pair of resistors;

said inverting input node connected to said potentiometer;

each of said second diode set electrically connected in series to said circuit output.

Appl. No. 10/619,852  
Amtd. Dated 25 October 2004  
Reply to Notice of Non-Compliant Amendment of October 21, 2004

- Rule 1.12
13. (Currently Amended) An electrical circuit for use with a chromatographic ionization detector for producing a stable voltage at a circuit output, comprising:  
an operational amplifier;  
said amplifier having an inverting input node and an amplifier output node;  
at least one light-emitting diode in series electrically connected to said amplifier output node;  
at least two forward-biased photovoltaic diodes arranged in electronically isolated diode pairs;  
each of said isolated diode pairs positioned in uniform operational proximity to one of said at least one light-emitting diode;  
each isolated diode pairs comprised of a first diode set and a second diode set;  
each of said first diode set electrically connected in series in a chargeable closed circuit to said inverting input node; and  
each of said second diode set electrically connected in series to said circuit output.

14. (Original) The electrical circuit for use with a chromatographic ionization detector in claim 13 further comprising:  
a reference voltage source;  
said first diode set connected to said reference voltage source.

Appl. No. 10/619,852  
Arndt Dated 25 October 2004  
Reply to Notice of Non-Compliant Amendment of October 21, 2004

15. (Original) The electrical circuit for use with a chromatographic ionization detector in claim

14 where:

said reference voltage source providing an adjustable voltage.

16. (Original) The electrical circuit for use with a chromatographic ionization detector in claim

13 further comprising:

a capacitor electrically connected to said amplifier output node and said inverting input node.

17. (Original) The electrical circuit for use with a chromatographic ionization detector in claim

16 further comprising:

said first diode set having a number of individual diodes in series; and

said inverting input node connected to first diode set intermediate an equal number of said individual diodes.

18. (Original) The electrical circuit for use with a chromatographic ionization detector in claim

13 further comprising:

a balanced pair of resistors connected in parallel with said first diode set; and

said inverting input node connected to said first diode set intermediate said balanced pair of resistors.

Appl. No. 10/619,852  
Amtd. Dated 25 October 2004  
Reply to Notice of Non-Compliant Amendment of October 21, 2004

19. (Original) The electrical circuit for use with a chromatographic ionization detector in claim

13 further comprising:

equivalent resistance connected in parallel with each of said first diode set and said second diode set.

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20. (Original) The electrical circuit for use with a chromatographic ionization detector in claim

13 further comprising:

resistance connected intermediate said amplifier output node and said at least one light-emitting diode.

21. (Currently Amended) A method for producing a stable voltage for use with a

chromatographic ionization detector comprising:

initiating a drive current through a drive circuit, said drive circuit containing an operational amplifier, said amplifier having an inverting input node and an amplifier output node, at least one light-emitting diode in series and said at least one light-emitting diode electrically receiving said drive current from said amplifier output node;

inducing at least two electrically isolated driven currents in at least one electrically isolated controlled circuit and at least one electrically isolated controlling circuit, said controlled circuit having at least one forward-biased photovoltaic diode, each said at least one photovoltaic diode arranged in electrically isolated pairs with at least one forward-biased photovoltaic diode of said controlling circuit each, each said at least one photovoltaic diode of each of said isolated diode pairs positioned in uniform operational proximity to one of said at least one light-emitting

Appl. No. 10/619,852

Amdt. Dated 25 October 2004

Reply to Notice of Non-Compliant Amendment of October 21, 2004

diode, each isolated diode pairs comprised of a controlling diode and a controlled diode, each of said controlling diodes electrically connected in series in a chargeable closed circuit to said inverting input node and each of said controlled diodes electrically connected in series to said circuit output;

stabilizing said electrically isolated driven current of each said at least one electrically isolated controlled circuit by adjusting said driven current with said drive current corrected by said operational amplifier for a correcting signal at said inverting input node.

22. (Original) The method of claim 21 wherein:

said controlling diodes generate said correcting signal to said inverting input node.

23. (Original) The method of claim 21 wherein:

said correcting signal is 0 when circuit is in a stable condition.

24. (Original) An electrical circuit for use with a chromatographic ionization detector for producing a stable voltage at a circuit output, comprising:

an operational amplifier;

said amplifier having an inverting input node and an amplifier output node;

at least one light-emitting diode in series electrically connected to said amplifier output node;

at least two photovoltaic diodes arranged in electronically isolated diode pairs;

Appl. No. 10/619,852  
Amtd. Dated 25 October 2004  
Reply to Notice of Non-Compliant Amendment of October 21, 2004

each of said isolated diode pairs positioned in uniform operational proximity to one of said at least one light-emitting diode;

each isolated diode pair comprised of a first diode set and a second diode set;

a balanced pair of resistors connected in parallel with said first diode set;

a potentiometer connected to said first diode set intermediate said balanced pair of resistors;

said inverting input node connected to said potentiometer;

each of said second diode set electrically connected in series to said circuit output.